

CLAIM LISTING

Claims 1 – 55 (Canceled)

56. (Previously presented) An integrated circuit, comprising:

a switch;

a capacitor; and

a conductive oxygen barrier located between said switch and said capacitor.

57. (Previously presented) An integrated circuit as in claim 56 and further including an insulator layer located between said switch and said capacitor and wherein: said capacitor includes a bottom electrode located on said conductive oxygen barrier; and

a portion of said conductive oxygen barrier is located on said insulator layer.

58. (Previously presented) An integrated circuit as in claim 57 wherein said conductive oxygen barrier includes a side edge portion and said insulator layer comprises a moat region, said moat region being defined partially by a moat sidewall and a moat bottom, and said moat sidewall including said side edge portion of said conductive oxygen barrier.

59. (Previously presented) An integrated circuit as in claim 58 wherein said insulator layer comprises an over-etched portion aligned with said conductive oxygen barrier side edge, and wherein said bottom-electrode side edge, said conductive oxygen barrier side edge, and said over-etched portion define said moat sidewall.

60. (Previously presented) An integrated circuit as in claim 59 and further including a nonconductive oxygen barrier layer located on said bottom-electrode side edge, said conductive-barrier side edge, and said over-etched portion.

61. (Previously presented) An integrated circuit as in claim 56 wherein said capacitor includes a ferroelectric material.

62. (Previously presented) An integrated circuit as in claim 61 wherein said ferroelectric material comprises a layered superlattice material.

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